

Please replace the paragraph beginning at page 10, line 13, with the following rewritten paragraph:

A2 Figure 2 is a block diagram of a conventional circuit for de-packetizing and processing data;

Please replace the paragraph beginning at page 10, line 15, with the following rewritten paragraph:

A3 Figure 3 is a block diagram of an embodiment of a device for practicing the present invention;

Please replace the paragraph beginning at page 10, line 19, with the following rewritten paragraph:

A4 Figure 5 is a flow chart illustrating an embodiment of part of the de-packetizing process according to the present invention.

Please replace the paragraph beginning at page 11, line 21, with the following rewritten paragraph:

A5 Figure 4 illustrates in more detail an embodiment of a device for de-packetizing and aligning input data according to the present invention. A packetized data source (not shown) is connected to a Data Input Interface 401 which provides necessary signaling for the external packetized data source as well as possible format conversion. For example, the data input interface may be an embodiment of a serial-to-parallel adaptor which converts serial format data such as the I2S format to parallel data. The Data Input Interface 401 is coupled to an Input FIFO 402.

In the Claims:

Please amend claims 1-29 to read as follows: